

08:30	<p align="center">FPGAworld 2018 Registration: Stockholm, Sep 18th, Frösundaleden 2A, 169 70 Solna, SWEDEN</p>		
<p align="center">Sponsors:     </p>			
09:00	<p align="center">Conference Opening Björn Eriksson, ÅF and Lennart Lindh, FPGAworld Room: Renen Program Proceedings</p>		
09:15-10:00	<p align="center">Keynote Title: Advanced verification and debug for large and complex FPGA designs Keynote speaker: Pieter J. Hazewindus, Synopsys, Mountain View, CA, USA</p> <p>Abstract: The FPGA industry is in a period of rapid change. There has been a significant growth in size and complexity of FPGAs with each new generation. This has given rise to several challenges to FPGA design which are now needing a more ASIC “like” design methodology. We survey the implications for designers implementing FPGAs, IP designers, as well as the tools supporting the FPGA designs. FPGAs have become more feasible as replacements for ASICs, and as stepping stones towards eventual ASIC implementations. The scale of integration challenges traditional testing methods, such as simulation. More complex clocking schemes require enhanced verification methodologies. And RTL debug of a multi-million gate FPGA design in reasonable time necessitates novel strategies.</p> <p align="right">Session Chair: Lennart Lindh, Room: Renen</p>		
10:00-10:30	<p align="center">Coffee Break & Exhibition (free to sponsor)</p>		
10:30-12:00	<p>Industrial/Hackers Program Session Chair: Kim Petersén Room: Renen</p>	<p>Academic Student Program Session Chair: Associate Professor Masoud Daneshtalab, Mälardalens University Room: Råven</p>	<p>Product/Industrial Session Chair: John Adair, Enterpoint, UK Room: Gasellen</p>
	<p>A: Adaptive Design of Optimized Deep Neural Networks for Embedded Systems Mohammad Loni, Mälardalens University, Sweden</p> <p>A: Deep learning application on edge FPGAs August von Hacht, Synectic Labs AB – Sweden</p> <p>A: Optimized Deep Learning on FPGAs Yasser Kilde Bajwa, Grazper Aps, Denmark</p>	<p>A: Data security in IoT application Christian Bangert, WinBond More information</p>	<p>C: Staying Competitive with modern FPGA Verification Methodologies (part 1) Stefan Bauer, Mentor - A Siemens Business Company More information</p> <p>C: Staying Competitive with modern FPGA Verification Methodologies (part 2) Stefan Bauer, Mentor - A Siemens Business Company More Information</p> <p>A: Scoreboarding Espen Tallaksen, Bitvis – Norway More information</p>
12:00-13:00	<p align="center">Lunch Break & Exhibition</p>		

13:00-13:30	<p align="center">Mike Dini talk, Dini Group, USA FPGA events during the year that has gone and gossips Session Chair: Lennart, Room: Renen</p>		
13:30-13:45	<p align="center">Break & Exhibition (free to sponsor)</p>		
13:45-14:45	<p align="center">Industrial/Product Session Chair: Lennart Room: Renen</p>	<p align="center">Industrial&Student/Hackers Session Chair: Magnus Karlsson, Innowicom System Solutions Room: Råven</p>	<p align="center">Product/Industrial Session Chair: Tommy Klevin, ÅF Room: Gasellen</p>
	<p>Tutorial: Flyover Cabling Solutions for High Performance Interconnect for FPGAs Room: Älgen</p>	<p>C: Essential tools for FPGA board bring up Nikolay Rognlien - Arrow Norway AS More information</p> <p>A: How Formal Techniques can keep hackers from driving you into a ditch Stefan Bauer, Mentor - A Siemens Business - Germany</p>	<p>A: Pistonhead – A BroadR-Reach camera for the automotive industry Bitsim, Sweden</p> <p>A: Use Vertical Integration in industrial applications with Programmable System on Chip Magnus Lindblad, XILINX</p>
14:45-15:15	<p align="center">Coffee Break & Exhibition</p>		
15:15-16:00	<p align="center">Keynote Title: FPGA – The Multifunction Accelerator of Choice Keynote speaker: Graham Copperwheat - Intel PSG located in UK.</p> <p>Abstract: Recently, we're living in an increasingly smart and connected world, a world that is generating increasing amounts of data and driven to find new ways to extract value from this data. A world that needs technology solutions that can not only meet today's demands but also tomorrows. FPGAs are stepping up to meet this challenge by writing the next chapter in the story of their evolution – FPGA as a reconfigurable multifunction accelerator. What are the characteristics of this new chapter, what are the strategies the FPGA industry is deploying to address the demand?</p> <p align="center">Session Chair: Lennart Lindh, Room: Renen</p>		
16:00-	<p align="center">Go Home Drink in the Exhibition hall</p>		
Exhibitors and Product Presenters Copenhagen and Stockholm	ÅF, Sweden DTU, Technical University of Denmark Aktuel Elektronik, Denmark Elektroniktidningen, Sweden Dini Group, USA Intel PSG, USA InnoFour, Netherlands	XILINX, USA Samtec inc. USA Winbond, Taiwan Synopsys, USA Avnet Silica, Denmark Avnet Silica, Sweden Synective Labs, Sweden Arrow, Europe Trenz Electronic, Germany	Bitvis, Norway Grazper, Denmark Shortlink, Sweden Mentor - A Siemens Business Bitsim, Sweden Motion Control, Sweden AGSTU, Sweden Enterpoint, United Kingdom
<p align="center">Welcome to next FPGAworld Conference 2019 Preliminary: Stockholm September 17 and Copenhagen September 19</p>			