

08:30	<p style="text-align: center;">FPGAworld 2018 Registration Sep 20th, DTU (SCION), Building 372, Diplomvej 2800 Lyngby</p> 
    	
09:00	<p style="text-align: center;">Conference opening Professor Lars Dittmann, Technical University of Denmark and Lennart Lindh, FPGAworld Program Proceedings</p>
09:15-10:00	<p style="text-align: center;">Keynote Title: Advanced verification and debug for large and complex FPGA designs Keynote speaker: Pieter J. Hazewindus, Synopsys, Mountain View, CA</p> <p>Abstract: The FPGA industry is in a period of rapid change. There has been a significant growth in size and complexity of FPGAs with each new generation. This has given rise to several challenges to FPGA design which are now needing a more ASIC “like” design methodology. We survey the implications for designers implementing FPGAs, IP designers, as well as the tools supporting the FPGA designs.</p> <p>FPGAs have become more feasible as replacements for ASICs, and as stepping stones towards eventual ASIC implementations. The scale of integration challenges traditional testing methods, such as simulation. More complex clocking schemes require enhanced verification methodologies. And RTL debug of a multi-million gate FPGA design in reasonable time necessitates novel strategies.</p> <p style="text-align: center;">Session Chair: Professor Lars Dittmann, Technical University of Denmark</p>
10:00-10:30	<p style="text-align: center;">Coffee Break & Exhibition</p>
10:30-12:00	<p style="text-align: center;">Product Presentations Session Chair: John Adair, Enterpoint, UK</p> <p>C: Staying Competitive with modern FPGA Verification Methodologies (part 1) Stefan Bauer, Mentor - A Siemens Business Company More information</p> <p>C: Staying Competitive with modern FPGA Verification Methodologies (part 2) Stefan Bauer, Mentor - A Siemens Business Company More Information</p> <p>C: Essential tools for FPGA board bring up Nikolay Rognlien - Arrow Norway AS More information</p>
12:00-13:00	<p style="text-align: center;">Lunch Break & Exhibition</p> <p style="text-align: right;">Sponsor</p> 

13:00-13:30	<p align="center">FPGA events during the year that has gone and gossips Mike Dini talk, Dini Group, USA</p> <p align="center">Session Chair: Lennart Lindh</p>		
13:30-14:30	<p align="center">Industrial Program Session Chair: Lennart</p> <p>A: Use Vertical Integration in industrial applications with Programmable System on Chip Ole Höjrup, XILINX</p> <p>A: Low Cost OpenCL Acceleration for Science and Engineering Projects John Adair, Enterpoint, UK More information</p>		
14:30-15:00	<p align="center">Coffee Break & Exhibition</p>		
15:00-16:00	<p align="center">Session Chair: Lennart</p> <p>Product Presentation: C: Data Security in IoT applications with Winbond's Authentication Flash Devices Christian Bangert, Winbond Electronics Corporation More information</p> <p>Industrial Presentation: A: Optimized Deep Learning on FPGAs Yasser Kilde Bajwa, Grazper Aps, Denmark</p>		
Exhibitors and Product Presenters Copenhagen and Stockholm	ÅF, Sweden DTU, Technical University of Denmark Aktuel Elektronik, Denmark Elektroniktidningen, Sweden Dini Group, USA Intel PSG, USA InnoFour, Netherlands	XILINX, USA Samtec inc. USA Winbond, Taiwan Synopsys, USA Avnet Silica, Denmark Avnet Silica, Sweden Synective Labs, Sweden Arrow, Europe Trenz Electronic, Germany	Bitvis, Norway Grazper, Denmark Shortlink, Sweden Mentor - A Siemens Business Bitsim, Sweden Motion Control, Sweden AGSTU, Sweden Enterpoint, United Kingdom
<p align="center">Welcome to next FPGAworld Conference 2019</p> <p align="center">Preliminary: Stockholm September 17 and Copenhagen September 19</p>			