

08:30	<p align="center">FPGAworld 2017 Registration; Stockholm, Sep 19th, Frösundaleden 2A, 169 70 Solna, SWEDEN www.FPGAworld.com</p>		
<p align="center">Sponsors:     </p>			
09:00	<p align="center">Conference Opening Björn Eriksson, ÅF and Lennart Lindh, FPGAworld Room: Renen Program Proceedings</p>		
09:15-10:00	<p align="center">Keynote speaker: Brendan Farley, XILINX Inc. Ireland RF Data Converters in an All Programmable MPSoC FPGA Session Chair: Lennart Lindh, FPGAworld Room: Renen</p>		
10:00-10:30	<p align="center">Coffee Break & Exhibition</p>		
10:30-12:00	<p>Industrial&Student/Hackers Session Chair: Kim Petersén Room: Renen</p>	<p>Academic Program Session Chair: Johan Alme Room: Råven</p>	<p>Product Program Session Chair: Lennart Room: Gasellen</p>
	<p>A: The Impact of Place and Route on FPGA Logic Synthesis Pieter J. Hazewindus, USA</p> <p>A: Portable Stimulus Specification - The Next Big Wave in Functional Verification Staffan Berg, Sweden More information</p> <p>A: Constrained Random and Functional Coverage for VHDL testbenches – controlled in a structured manner Espen Tallaksen, Norway</p>	<p>B: Use of Analog Signatures for Hardware Trojan Detection Taimour Wehbe¹, Vincent J. Mooney, David Keezer, Omer T. Inan^{1,3}, Abdul Qadir Javaid¹ and Chinmoy Kulkarni¹, USA Paper (pdf)</p> <p>B: Synthesis of VLIW Accelerators from Formal Descriptions in a Real-Time Multi-Core Environment Johnny Öberg, KTH, Sweden Paper (pdf)</p> <p align="center"><u>Industrial paper</u></p> <p>A. Highly optimized streaming FFTs for FPGAs Carl Ingemarsson, Petter Källström, and Oscar Gustafsson, Linköping University, Sweden</p>	<p>C: Mastering Clock Domain Crossing challenges in FPGA Design Stefan Bauer, InnoFour, Netherlands More information</p> <p>C: Input power related challenges Thomas Ginell, Linear Technology now part of Analog Devices</p> <p>C: Xilinx Cost-Optimized Portfolio Spartan-7, Zynq Z-7000S Per Boström Avnet Silica</p>
12:00-13:00	<p align="center">Lunch Break & Exhibition</p>		

13:00-13:30	<p align="center">Mike Dini talk, Dini Group, USA FPGA events during the year that has gone and gossips Session Chair: Lennart Room: Renen</p>		
13:30-13:45	<p align="center">Break & Exhibition</p>		
13:45-14:45	<p>Industrial&Student/Hackers Session Chair: Björn Eriksson Room: Renen</p>	<p>Industrial Program Session Chair: Hichem Belhadj Room: Råven</p>	<p>Student/Hackers Session Chair: Lennart Room: Gasellen</p>
	<p>A: Common RTOS-related bugs - How avoid and detect Johan Kraft, Sweden</p> <p>A: FPGA in Neuroscience Lars Forsberg, Sweden, Synective Labs AB</p>	<p>A: Context-Aware Logic Replication for Higher Speed and Lower Power Microsemi Corp. USA</p> <p>A: Security-Conscious FPGA Design: The Rule and Not the Exception Anymore! Microsemi Corp. USA</p>	<p>A: Interface and visualization for accelerometer in VHDL (15 min) Anders Axelsson, Sweden Design report</p> <p>A: Study on usage of free IP for serial communication in the context of Cyclone-V SoC HPS/FPGA and Linux (15 min) Magnus Karlsson, Sweden Part of the design report Design report, PPT</p> <p><u>Product Presentation</u> C: Intel High Level Synthesis Nikolay Rognlien, Arrow</p>
14:45-15:15	<p align="center">Coffee Break & Exhibition</p>		
15:15-16:00	<p align="center">Keynote Title: Programmable Technologies: New Challenges and New Opportunities Speaker: Hichem Belhadj, Chief Systems Architect – CTO Office, Microsemi Corp. USA Session Chair: Lennart Lindh Room: Renen</p>		
16:00-	<p align="center">Go Home Drink in the Exhibition hall</p>		
Exhibitors and Product Presenters Copenhagen and Stockholm	<p>ÅF, Sweden DTU, Technical University of Denmark Aktuel Elektronik, Denmark Elektroniktidningen, Sweden Prevas, Sweden XILINX, USA</p>	<p>Linear Technology, USA Dini Group, USA Innofour, Netherlands Avnet Silica, Denmark Avnet Silica, Sweden Synective Labs</p>	<p>Arrow, Europe SAMTEC, USA Silicom, Denmark Motion Control, Sweden AGSTU Education, Sweden Analog Devices, USA Elmatica, Norway</p>
<p align="center">Welcome to next FPGAWorld Conference 2018 Stockholm 18 September and Copenhagen 20 September</p>			