08:30	**FPGAworld 2017 (Working)** **Registration; Stockholm, Sep 19th, Frösundaleden 2A, 169 70 Solna, SWEDEN.** **www.FPGAworld.com**		
Sponsors:    			
09:00	**Conference Opening** ÅF and Lennart Lindh, FPGAworld Room: Renen		
09:15 – 10:00	Keynote speaker: **Brendan Farley, XILINX Inc. Ireland** Title: **RF Data Converters in an All Programmable MPSoC FPGA** **Abstract:** Recent state-of-the-art FPGAs have seen the integration of multi-giga-sample RF data converters to address the requirements of next generation wideband digital communications system. The keynote presentation will give an overview of the RFSoC FPGA which integrates such functionality and will discuss some potential applications and future trends **Brendan Farley** is a Senior Director of Engineering at US multinational technology corporation Xilinx Inc. where he is responsible for Analog and Digital-RF Research and Development. Brendan holds a Bachelor Degree in Electronic Engineering from Trinity College Dublin and a Master of Science Degree in Technology Management from NUI Galway. Session Chair: Associate Professor Lennart Lindh, FPGAworld Room: Renen		
10:00	**Coffee Break & Exhibition**		
10:30 - 12:00	Industrial&Student/Hackers Session Chair: Room: Renen Abstracts	Academic Program, Session Chair: Room: Räven Coming	Product Program Session Chair: Room: Gasellen Abstracts
	A1: **Constrained Random and Functional Coverage for VHDL testbenches – controlled in a structured manner** Espen Tallaksen, Norway A2: **The Impact of Place and Route on FPGA Logic Synthesis** Pieter J. Hazewindus, USA A3: Free	B1: B2: B3:	C1: **Mastering Clock Domain Crossing challenges in FPGA Design** Stefan Bauer, Netherlands InnoFour, More information C2: Booked; Linear Technology, More to come! C3: Booked: Synective Labs, More to come!
12:00	**Lunch Break & Exhibition**		
13:00-13:30	**Mike Dini,** Dini Group, USA More to come!		

	Industrial&Student/Hackers Session Chair: Room: Renen Abstracts	Academic Program, Session Chair: Room: Räven	Product Program Session Chair: Room: Gasellen Abstracts
13:30 - 14:30	Student projects: A4: **Breakout Game with DE1-SOC board (15 min), VHDL and processor** Navid Kheradmand, Sweden Presentation, Design report A5: **Interface and visualization for accelerometer (30 min), only VHDL** Anders Axelsson, Sweden Design report A6: Free (15 min)	B4: B5:	C5: "Booked" Arrow, More to come! C6: Free
14:30	**Coffee Break & Exhibition**		
	Industrial&Student/Hackers Session Chair: Room: Renen Abstracts	Academic Program, Session Chair: Room: Räven	Product Program Session Chair: Room: Gasellen Abstracts
15:00 – 16:00	A7: Free A8: Free	B6: B7:	C7: Free C8: Free
16:00 - 16:30	Panel Discussion **Topic: ?** Panel: ? (If you are interested, please let me know, Lennart Lindh) Session Moderator: **Per Henricsson**, Elektroniktidningen, Sweden Room: Renen		
16:30	**Go Home Drink in the Exhibition hall**		
Exhibitors and Product Presenters	ÅF, Sweden DTU, Technical University of Denmark Aktuel Elektronik, Denmark Elektroniktidningen, Sweden Prevas, Sweden XILINX, USA	Linear Technology, USA Dini Group, USA Innofour, Netherlands Terasic, Taiwan Avnet Silica, Denmark Avnet Silica, Sweden Synective Labs	Arrow, Europe Motion Control, Sweden AGSTU education, Sweden