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| 08:30 | <p align="center">FPGAworld 2017 (Working) Registration; Stockholm, Sep 19th, Frösundaleden 2A, 169 70 Solna, SWEDEN. www.FPGAworld.com</p> | | |
| <p align="center">Sponsors:     </p> | | | |
| 09:00 | <p align="center">Conference Opening ÅF and Lennart Lindh, FPGAworld Room: Renen</p> | | |
| 09:15 – 10:00 | <p align="center">Keynote Title: RF Data Converters in an All Programmable MPSoC FPGA Speaker: Brendan Farley, XILINX Inc. Ireland</p> <p align="center">Session Chair: Lennart Lindh, FPGAworld Room: Renen</p> | | |
| 10:00-10:30 | <p align="center">Coffee Break & Exhibition</p> | | |
| 10:30 - 12:00 | <p align="center">Industrial&Student/Hackers Session Chair: Room: Renen Abstracts</p> | <p align="center">Academic Program, Session Chair: Room: Råven Coming Papers coming</p> | <p align="center">Product Program Session Chair: Room: Gasellen Abstracts</p> |
| | <p>A1: The Impact of Place and Route on FPGA Logic Synthesis Pieter J. Hazewindus, USA</p> <p>A3: Portable Stimulus Specification - The Next Big Wave in Functional Verification Staffan Berg, Sweden</p> <p>A3: Constrained Random and Functional Coverage for VHDL testbenches – controlled in a structured manner Espen Tallaksen, Norway</p> | <p>B1: Use of Analog Signatures for Hardware Trojan Detection Taimour Wehbe¹, Vincent J. Mooney, David Keezer, Omer T. Inan^{1,3}, Abdul Qadir Javaid¹ and Chinmoy Kulkarni¹, USA</p> <p>B2: Synthesis of VLIW Accelerators from Formal Descriptions in a Real-Time Multi-Core Environment Johnny Öberg, KTH, Sweden</p> <p>Industrial paper B3: FPGA in Neuroscience Lars Forsberg, Sweden, Synective AB</p> | <p>C1: Mastering Clock Domain Crossing challenges in FPGA Design Stefan Bauer, Netherlands InnoFour, More information</p> <p>C2: Booked; Linear Technology, More to come!</p> <p>C3: Booked; Arrow, More to come!</p> |
| 12:00–13:00 | <p align="center">Lunch Break & Exhibition</p> | | |
| 13:00-13:30 | <p align="center">Mike Dini talk, Dini Group, USA FPGA events during the year that has gone and gossips Session Chair: Lennart Lindh</p> | | |

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| | Student/Hackers Session Chair: Room: Renen Abstracts | Industrial&Student/Hackers Session Chair: Room: Råven | Product Program Session Chair: Room: Gasellen Abstracts |
| 13:30 - 14:30 | Student projects: A4: Breakout Game with DE1-SOC board (15 min), VHDL and processor Navid Kheradmand, Sweden Presentation , Design report A5: Interface and visualization for accelerometer (30 min), only VHDL Anders Axelsson, Sweden Design report A6: Free (15 min) | B4: Reserved B5: Reserved | C5: Booked; Synective AB C6: |
| 14:30 -15:00 | Coffee Break & Exhibition | | |
| 15:00 - 15:45 | Keynote Title: Programmable Technologies: New Challenges and New Opportunities Speaker: Hichem Belhadj, Chief Systems Architect – CTO Office, Microsemi Corp. USA Room: Renen | | |
| | Industrial&Student/Hackers Session Chair: Room: Renen Abstracts | Industrial&Student/Hackers Session Chair: Room: Råven | Product Program Session Chair: Room: Gasellen Abstracts |
| 15:45–16:15 | A7: Reserved A8: Reserved | B6: B7: | C7: Free C8: Free |
| 16:30- | Go Home Drink in the Exhibition hall | | |
| Exhibitors and Product Presenters Copenhagen and Stockholm | ÅF, Sweden DTU, Technical University of Denmark Aktuell Elektronik, Denmark Elektroniktidningen, Sweden Prevas, Sweden XILINX, USA | Linear Technology, USA Dini Group, USA Innofour, Netherlands Terasic, Taiwan Avnet Silica, Denmark Avnet Silica, Sweden Synective Labs | Arrow, Europe SAMTEC, USA DOULOS, United Kingdom Motion Control, Sweden AGSTU education, Sweden |