

08:30	<p style="text-align: center;">FPGAworld 2015 Registration Sep 10th DTU (SCION), Building 372, Diplomvej 2800 Lyngby</p>
<p>Sponsors:       </p>	
09:00	<p style="text-align: center;">Conference opening Lars Dittmann, Technical University of Denmark and Lennart Lindh, FPGAworld</p>
09:15	<p style="text-align: center;">Key Note Session</p> <p style="text-align: center;">Key Note Speaker: Professor Vincent Mooney III, Georgia Institute of Technology, Atlanta, USA</p> <p style="text-align: center;">Hardware Security and FPGAs: Strategies and Counterattacks</p> <p>Recent highly publicized security attacks on businesses and governments have heightened the awareness of hardware vulnerability to malicious attacks. FPGA technology offers a unique strategy not available to application-specific non-programmable hardware: reconfiguration. This talk will give an overview of recent results in hardware security including an approach based on hardware signatures. The ability of hardware reconfiguration to protect run-time hardware and software highlights the advantages of FPGAs.</p> <p style="text-align: center;">Session Chair: Lars Dittmann, Technical University of Denmark</p>
10:00	<p style="text-align: center;">Coffee&Tea Break, Sponsor: </p>
10:30 – 12:30	<p style="text-align: center;">Product Program, C1-5 Session Chair: Ove Boström, FPGAworld Abstracts-C</p> <p>C1: Optimizing HW/SW partition of a complex embedded system using C/C++ Olivier Tremois, EMEAI DSP Specialist FAE, XILINX, USA</p> <p>C2: Max10 - The next step in low cost FPGA integration Presenters: Nikolay Rognlien, MDE Programmable Logic, Arrow Norway AS, (more information), info 2</p> <p>C3: Designing power for FPGAs, Nicolai Mahncke - Field Application Engineer at Linear Technology</p> <p>C4: UVM Framework – an easy way to improve your verification job Stefan Bauer (Mentor Graphics), InnoFour (more information)</p>
12:30	<p style="text-align: center;">Lunch Break & Exhibition</p>

<p>13:30 – 15:00</p>	<p>Industrial and Student/Hackers Program, A1-3 Session Chair: David Källberg, FPGAworld Abstracts-A</p> <p>A1: Automotive ADAS feature saves lifes with flexible HW (FPGA etc.) – practical implementation reference Tryggve Mathiesen, Qamcom R&T AB, Sweden</p> <p>A2: Accelerating embedded software development and ASIC verification with FPGAs Juergen Jaeger, Cadence Design Systems, USA, (more information)</p> <p>A3: High-speed FPGA-based stereovision system - a success story Rafal Kapela, Ph.D.,Antmicro Ltd/Poznan University of Technology, Polan</p>		
<p>15:00</p>	<p>Coffee Break</p>		
<p>15:30 – 17:00</p>	<p>Industrial and Student/Hackers Program, A4-6 Session Chair: Ove Boström and David Källberg, FPGAworld Abstracts-A</p> <p>A4: OSVVM for VHDL Verification Jim Lewis, SynthWorks Design Inc, USA, more information, Course</p> <p>A5: Real-time operating system, hardware or software? André Norberg, AGSTU, Sweden, more information</p> <p><u>Product Presentation</u></p> <p>C5: SmartFusion2 – Embedded system "Root-of-Trust" Secure Boot Demonstration Peter Trott, Microsemi, USA, (more information)</p>		
<p>17:00 – about 17:30</p>	<p>Panel Discussion</p> <p>What will be the impact of Intel on Altera? Any reason to be frightened? Xilinx next?</p> <p>Session Moderator: Rolf Sylvester-Hvid, Aktuel Elektronik, (Danish Magazine) Opinion presenter: Mike Dini, Dini Group, USA (10 minutes without interruption) Panel: The participants in the room!</p>		
<p>Exhibitors and Product Presenters Copenhagen and Stockholm</p>	<p>ÅF, Sweden Aktuel Elektronik, Denmark Elektroniktidningen, Sweden The Dini Group, USA ELMATICA AS, Norway</p>	<p>Exostiv Labs, Belgium Linear Technology, USA Silica, USA XILINX, USA Synective Labs, Sweden</p>	<p>Microsemi , USA Arrow Electronics, USA Altera,USA InnoFour, Netherlands AGSTU AB, Sweden</p>