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Title: Working with Xilinx Zynq efficiently

Abstract: Recent chips with ARM based processors and FPGA logic provide potential for many applications. IP cores and operating systems (OS) have been prepared to simplify development. However, the integration of IP cores and OS is not covered by any development tool yet. We propose universal Reconfigurable System on Chip (RSoC) Framework to support rapid prototyping of applications on these chips. Application can run in FPGA and/or in processor and RSoC Framework covers all mutual communication.

Length: 15 min