

**Title: Bridging the FPGA World and the PCB**

Abstract: Today's powerful and extremely high pin-count FPGA's provide engineers with significant opportunities for increased features and more functionality whilst reducing the cost of their products. But with this increased complexity comes significant challenges in integrating these devices onto the Printed Circuit Board, with the need to map hundreds of logical signals to the physical pin out of the device whilst maintaining the electrical integrity of the design. This session will show not only how to ease the FPGA-on-board integration process by bridging the two worlds of HDL and PCB design but also how to guarantee the most optimum FPGA I/O design and physical implementation on the PCB significantly reducing both time-to-market and manufacturing costs.



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