

Title: Silicon and Software for Easy 20nm Designs
From: Craig Davis, Altera

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Arria(r) 10 FPGAs and SoCs deliver amazing amounts of programmable computation both in FPGA logic (including the new hard Floating-Point DSP blocks) and in the dual-core ARM Cortex A9 processor. Coupled with the right tools; OpenCL(tm), DSP Builder and Quartus(r) II software, implementation of designs in Arria 10 devices is fast & easy. Attend this presentation to learn about the features of these tools and Arria 10 FPGAs and SoCs.