

08:30	<p align="center">FPGAworld 2014 Registration; Stockholm, Sep 9th, Frösundaleden 2A, 169 70 Solna, SWEDEN. www.FPGAworld.com</p>		
09:00	<p align="center">Conference Opening Thony Johansson, ÅF and Lennart Lindh, FPGAworld Room: Renen</p>		
09:15 – 10:00	<p align="center">Key Note Session Field Programmable SoCs: The Greatest Acceleration of the Innovation Race Key Note Speakers: Hichem Belhadj, Microsemi Corp, USA Hichem Belhadj has been involved in the Programmable Logic Industry since the late 80s. He has been with Microsemi SoC (former Actel) for over 15 years. Hichem has recently moved to Microsemi Corporate to lead the Field Systems and Applications Engineering. He holds a Master and a PhD from the Polytechnic Institute of Grenoble. Session Chair: Lennart Lindh, FPGAworld Room: Renen</p>		
10:00	<p align="center">Coffee Break & Exhibition</p>		
10:30 – 12:30	<p>Industrial and Student/Hackers Program Session Chair: Kim Petersén, HDC AB Room: Renen</p>	<p>Academic Program, B1-4 Session Chair: Jaán Raik, Tallinn University of Technology Room: Råven Front page, Abstracts</p>	<p>Product Program Session Chair: Ove Boström, FPGAworld Room: Storcken</p>
	<p>A1: VUnit - An Automated Testing Framework for VHDL Lars Asplund, Synective Labs, Sweden, Abstract</p> <p>A2: SimXMD: Simulation-based HW/SW Co-Debugging for FPGA Embedded Systems Ruediger Willenberg, University of Toronto, Canada, Abstract More Information</p> <p>A3: Achieving the right FPGA design quality – Could reviews save the day? Bitvis AS, Espen Tallaksen, Norway, Abstract</p> <p>A4: A CORDIC Processor Implemented on a DE2-115 Board, (15 min) Martin Sjödin, Ericsson and AGSTU School of Higher Vocational Education, Sweden, Sweden, Abstract</p> <p>A5: Working with Xilinx Zynq efficiently, (15 min) Jan Viktorin and Pavol Korcek, Brno University of Technology, Czech Republic, Abstract</p>	<p>B1: Performance Evaluation of Dynamic Circuit Specialization on Xilinx FPGAs Dirk Stroobandt, Tom Davidson, Karel Heyse, Amit Kulkarni - ELIS department, Computer Systems Lab, Ghent University, Belgium, More information</p> <p>B2: Optimizing Memory Power in Hybrid ARM-FPGA Chips With Lossless Data Compression Peng Sun, Jose Nunez-Yanez,</p> <p>B3: The Track Engine an FPGA Implementation of a Track-finding Algorithm for the IceCube Neutrino Telescope Carl Wernhoff,</p> <p>B4: Going for Brain-Scale Integration using FPGAs, TSVs and NOC based Artificial Neural Networks: A Case Study Nowshad Painda Mand, Johnny Öberg - KTH Royal Institute of Technology, Sweden,</p>	<p>C1: Synopsys HAPS Developer eXpress FPGA-based Prototyping Solution Antti Innamaa, Synopsys, USA, Abstract</p> <p>C2: Digital Power System management for FPGAs Mats Hellberg, Linear Technology, USA, Abstract</p> <p>C3: Bridging the FPGA World and the PCB Rick Stroot, InnoFour, Netherlands, Abstract</p> <p>C4: Silicon and Software for Easy 20nm Designs Jonas Åström, Altera, Abstract</p>

12:30	Lunch Break & Exhibition		
13:30 - 15:00	Industrial program Session Chair: Johnny Öberg , KTH, Sweden Room: Renen	Industrial and product program Session Chair: Room: Råven	Product Program Session Chair: Ove Boström , FPGAworld Room: Storken
	<p>A6: The Gates are Open: An overview of the OpenRISC Ecosystem Olof Kindgren, Qamcom Research & Technology, Abstract, More information</p> <p>A7: Bufferless Video Encoding for Biomedical Applications on sub-one-dollar FPGA Devices Mario Vigliar, DPControl Srl - I3C srl - University of Salerno, Italy, Abstract</p> <p>A8 : Hardware Automation of Scheduler, Placer, Inter-Task Communications and IO System Functions for Manycore Processors Dynamically Shared among Multiple Applications Mark Sandstrom, ThroughPuter, Inc, USA, Abstract, Paper and PPT</p>	<p>A9: Use of FPGAs in Cryptocurrencies, (1 hour) Mike Dini, Dini Group, USA, Abstract, More information</p> <p style="text-align: center;">Product Program, C8</p> <p>C8: Spacetime: High performance design made easy, Tabula, Abstract</p>	<p>C5: Introducing the world's only high density FPGA built using Intel's 22nm FinFet technology Les MacInnes, Achronix, Abstract</p> <p>C6: UVVM: Universal VHDL Verification Methodology ("UVM for VHDL") Bitvis AS, Espen Tallaksen, Norway, Abstract</p> <p>C7: FPGA Best Practices - A pragmatic approach to faster and better development Bitvis AS, Espen Tallaksen, Norway, Abstract</p>
15:00	Coffee Break & Exhibition Sponsored by AGSTU AB		
15:30 - 16:30	<p>Panel Discussion FPGAs - 10 years from now Session Moderator: Per Henricsson, Elektroniktidningen (Swedish Magazine)</p> <p>Panel: Jaan Raik, Tallinn Mike Dini, Dini Group, USA</p> <p>Room: Renen</p>		
16:30	<p>Go Home Drink in the Exhibition hall Sponsored by FPGAworld</p> <p>Sponsors FPGAworld 2014:</p> <div style="display: flex; justify-content: center; gap: 10px;">      </div>		