









08:30	<h2 style="margin: 0;">FPGAworld 2014</h2> <p style="margin: 0;">Registration Sep 11th DTU (SCION), Building 372, Diplomvej 2800 Lyngby</p>	
09:00	<h3 style="margin: 0;">Conference opening</h3> <p style="margin: 0;">Lennart Lindh, FPGAworld and Lars Dittmann, Technical University of Denmark</p>	
09:15	<h3 style="margin: 0;">Key Note Session</h3> <h4 style="margin: 0;">Field Programmable SoCs: The Greatest Acceleration of the Innovation Race</h4> <p style="margin: 0;">Key Note Speakers: <b>Hichem Belhadj</b>, Microsemi Corp, USA</p> <p style="margin: 0; font-size: small;">Hichem Belhadj has been involved in the Programmable Logic Industry since the late 80s. He has been with Microsemi SoC (former Actel) for over 15 years. Hichem has recently moved to Microsemi Corporate to lead the Field Systems and Applications Engineering. He holds a Master and a PhD from the Polytechnic Institute of Grenoble.</p> <p style="margin: 0; font-size: small;">Session Chair: Lars Dittmann, Technical University of Denmark</p>	
10:00	<h3 style="margin: 0;">Coffee&amp;Tea Break</h3> <p style="margin: 0;">Sponsored by <b>DTU</b>, Technical University of Denmark</p>	
10:30 – 12:30	<p style="margin: 0;">Product Program, C1-5 Session Chair: Ove Boström, FPGAworld</p> <p style="margin: 0;"><b>C1: Synopsys HAPS Developer eXpress FPGA-based Prototyping Solution</b> Antti Innamaa, Synopsys, USA, <a href="#">Abstract</a></p> <p style="margin: 0;"><b>C2: Development of a 10G Ethernet UDT Server in FPGA</b> Steinn Gustafsson, Chevin Technology, United Kingdom, <a href="#">Abstract</a>, <a href="#">More information</a></p> <p style="margin: 0;"><b>C3: Introducing the world's only high density FPGA built using Intel's 22nm FinFet technology</b> Les MacInnes, Achronix, <a href="#">Abstract</a></p> <p style="margin: 0;"><b>C4: Bridging the FPGA World and the PCB</b> Rick Stroot, InnoFour, Netherlands, <a href="#">Abstract</a></p>	
12:30	<h3 style="margin: 0;">Lunch Break &amp; Exhibition</h3> <p style="margin: 0;">Sponsored by <b>DTU</b>, Technical University of Denmark</p>	
13:30 – 15:15	<p style="margin: 0;">Industrial and Student/Hackers Program, A1-3 Session Chair:</p> <p style="margin: 0;"><b>A1: Use of FPGAs in Cryptocurrencies</b> Mike Dini, Dini Group, USA, <a href="#">Abstract</a>, <a href="#">More information</a></p> <p style="margin: 0;"><b>A2: SimXMD: Simulation-based HW/SW Co-Debugging for FPGA Embedded Systems</b> Ruediger Willenberg, University of Toronto, Canada, <a href="#">Abstract</a>, <a href="#">More information</a></p>	

	<p>A3: <b>The Gates are Open: An overview of the OpenRISC Ecosystem</b>  Olof Kindgren, <a href="#">Abstract</a>, <a href="#">More information</a></p> <p>A4; <b>Working with Xilinx Zynq efficiently</b>, (15 min)  Jan Viktorin and Pavol Korcek, Brno University of Technology, Czech Republic, <a href="#">Abstract</a></p>	
15:15	<p style="text-align: center;"><b>Coffee Break</b>  Sponsored by <b>DTU</b>, Technical University of Denmark</p>	
15:30 – 17:00	<p style="text-align: center;">Academic Program, B1-3  Session Chair: <b>Johnny Öberg</b>, KTH, Sweden  <a href="#">Front page</a>, <a href="#">All abstracts</a></p> <p><b>B1: A Multi-Paradigm Approach to Teaching Students Embedded Systems Design using FPGAs and CPLDs</b> David C. Dyer - School of Engineering, University of Warwick, UK, Yan Lin Aung - CHiPES Research Centre, Nanyang Technological University, Singapore, <a href="#">More information</a></p> <p style="text-align: center;">Product Program, C 5-6  Session Chair: Ove Boström, FPGAworld</p> <p><b>C5: Silicon and Software for Easy 20nm Designs</b>  Rune Domsten, Altera, <a href="#">Abstract</a></p> <p><b>C6: Spacetime: High performance design made easy</b> <b>Tabula</b>, <a href="#">Abstract</a></p>	
17:00 – about 17:30	<p style="text-align: center;">Panel Discussion  <b>MPSoC on FPGAs</b>  Session Moderator: <b>Rolf Sylvester-Hvid</b>, Aktuell Elektronik, (Danish Magazine)</p> <p style="text-align: center;"><b>Panel:</b>  Johnny Öberg, KTH, Sweden  Jens Sparsö, DTU, Denmark  Martin Shoeberl, DTU, Denmark</p>	
	<p style="text-align: center;"><b>Sponsors FPGAworld 2014:</b></p> <div style="display: flex; justify-content: center; gap: 10px;">      </div>	