

The evolvement of the FPGA designer

A panel discussion

Abstract:

As the FPGAs continues to evolve, integrating more functions towards SoC solution the expectations of what knowledge the FPGA designer should contribute with in the systems partitioning is increasing.

- What skillset is required from the FPGA developer to be able to support the Systems Designers moving forward?
- How will the FPGA designer acquire the skillset required?
- To what knowledge level shall the FPGA designer support software issues e.g. BSP, test applications, OS installation etc.?