

Title:

Faster, Cheaper and Safer FPGA Development - A practical approach.

Presenter:

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PPT:

http://bitvis.no/media/1934/Backgrounder_FPGA_dev_Best_Practices.pdf

About the presenter:

During twenty years Espen has had a special interest for methodology cultivation and pragmatic efficiency and quality improvement. One result of this interest has been a verification system that is currently being used by many Norwegian companies (+FOI in Sweden) and now released as open source. He has given numerous presentations in Scandinavia on various technical aspects of FPGA development. He is also the initiator and chair of the Norwegian FPGA-forum. In Sweden Espen has given several courses and technical presentations (including keynote and 5 industrial presentations at FPGAworld)

Abstract:

Have you ever experienced delays in FPGA projects?

Have you ever detected bugs too late in (or after) an FPGA project?

Have you or other team members ever spent more man-hours than scheduled in an FPGA project?

Many tool vendors try to convince you they have a solution for improving this, - and some of the tools do help - a bit....

However, - the ONLY way to SIGNIFICANTLY improve your FPGA development time, cost and risk - is to improve the way you and your team work when developing an FPGA - almost independent of expensive tools.

It is all a question of knowledge, awareness, methodology, internal development support, quality assurance and structure at all levels.

It is also a question of how you apply this to your own work - and to the project.

There are lots of approaches to improving schedule and quality, but most of them have a wrong focus or do not work in a real project. A pragmatic and practical approach is required to get significant improvements.

The good thing about this is that no expensive tool is required. It is only a matter of applying experience and common sense in a structured manner.

This presentation is meant as a teaser to our two day course 'FPGA development Best Practices'

The presentation summarizes the normal challenges of FPGA development, gives an overview of solutions, and tells you how this is covered in our course.

The course has previously been held in Linköbing, Stockholm, Copenhagen and several places in Norway, as well as on-site, for instance at TransMode Systems in Sweden.

The feedback from the participants has always been very good, and in Norway around 50% of all full time FPGA developers have attended the course.

You may read my presentation 'Bugs and Problems - Worst disasters' (Linked in here - or available from www.bitvis.no > events) from FPGAworld 2009 to get a better understanding of the FPGA development challenges.

The course will be held in Sweden given sufficient registration.
Dates and locations may be discussed after the presentation.

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