

# Call for FPGAworld Conference

## September 2013

International Academic/Industrial Papers, Product Presentations,  
Exhibits, Demonstrations, Panel session and Tutorials  
**Stockholm, Copenhagen and Finland**

### **Submissions should be in at least one of these areas**

- DESIGN METHODS - MODELS AND PRACTICES
  - Project methodology
  - Design methods as Hardware/software co-design
  - Modeling of different abstraction
  - IP component designs
  - Interface design: supporting modularity
  - Integration - models and practices
  - Verification and validation
  - Board layout and verification
- TOOLS
  - News
  - Design, modeling, implementation, verification and validation
  - Instrumentation, monitoring, testing, debugging, etc.
  - Synthesis, compilers and languages
- HW/SW IP COMPONENTS
  - New IP components for platforms and applications
  - Real-time operating systems, file systems, internet communications
- PLATFORM ARCHITECTURES
  - Embedded systems
  - Single/multiprocessor architecture
  - Memory architectures
  - Reconfigurable architectures
  - HW/SW architecture
  - Low power architectures
- APPLICATIONS
  - Case studies from users in industry, academic and students
  - HW/SW component presentation
  - Prototyping
- SURVEYS, TRENDS AND EDUCATION
  - History and surveys
  - Tutorials
  - Student works and projects

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**FPGA**<sup>TM</sup>  
**world**



# Welcome to 9<sup>th</sup> **FPGAworld CONFERENCE!**

## September the 4<sup>th</sup> 2012 Stockholm, ÅF, Solna

The international FPGAworld Conference addresses all aspects of digital and hardware/software system engineering on FPGA technology. It is a discussion and network forum for researchers and engineers working on industrial and research projects, state-of-the-art investigations, development and applications. For further information, please visit [www.fpgaworld.com](http://www.fpgaworld.com) and [www.facebook.com/fpgaworld](http://www.facebook.com/fpgaworld).

### **EXHIBITORS**

Altera, Synopsys, Bitsim, Avnet-Memec , ORSoC, Xelmo, ÅF, Silica, Motion Control, Dini Group, Elektroniktidningen, Innofour, Annapolis Micro Systems, Synective Labs

### **SPONSORS**



08:30 - 09:00	Registration		
09:00 - 09:15	Conference opening Lennart Lindh, FPGAworld		
09:15 - 10:00	Key Note Session <a href="#"><u>Managing FPGA design complexity: a software perspective</u></a> <a href="#"><u>Dr. Pieter Hazewindus, Synopsys</u></a> Room: Renen		
10:00 - 10:30	Coffee Break <b>Sponsored by ÅF</b>		
	Room: Renen Session Chair Tryggve Mathiesen, InformASIC	Room: Älgen Session Chair Tommy Klevin, ÅF	
10:30 - 12:00	Session A1 <a href="#"><u>FPGA Verification &amp; Development Environment</u></a>  Session A2 <a href="#"><u>Extend the performed simulations to get more robust designs, in a cost effective and time saving way</u></a>  Session A3 <a href="#"><u>Migen, a Python toolbox for building complex digital hardware</u></a>	Session C1 <a href="#"><u>The impact of requirement traceability in safety critical designs</u></a>  Session C2 <a href="#"><u>Microsemi SmartFusion SOC</u></a>  Session C3 <a href="#"><u>Product Presentation: Silica Zynq-7000 All Programmable SoC</u></a>	
12:00 - 13:00	Lunch Break		

	<p>Room: Renen Session Chair Kim Petersén, HDC</p>	<p>Room: Älgen Session Chair Johnny Öberg, KTH</p>
13:00 - 14:30	<p>Session A4 <a href="#"><u>Will ARM based FPGA SOC change system design Methodology?</u></a></p> <p>Session A5 <a href="#"><u>Rapid Video Prototyping</u></a></p> <p>Session A6 <a href="#"><u>Design reuse of both HW an SW - will this finally take off?</u></a></p>	<p>Session B1 <a href="#"><u>A Tag-Based Dynamic Memory Map Architecture in VHDL</u></a></p> <p>Session B2 <a href="#"><u>A 26 ps RMS time-to-digital converter core for Spartan-6 FPGAs</u></a></p> <p>Session B3 <a href="#"><u>Implementation of Genetic Algorithms using FPGA Technology</u></a></p>
14:30 - 15:00	Coffee Break	
15:00 - 16:00	<p>Room: Renen Session Chair Yehoshua Shoshan, Innofour</p> <p>Session A7 <a href="#"><u>FPGA SoCs - A Centerpiece For Digital Design</u></a></p> <p>Session A8 <a href="#"><u>Model-Based Design for FPGAs - Fact or Fiction?</u></a></p>	<p>Room: Älgen Session Chair Johnny Öberg, KTH</p> <p>Session B4 <a href="#"><u>Behavioral specification diversification for logic controllers implemented in FPGA devices</u></a></p> <p>Session C8 <a href="#"><u>Product Presentation: TBA</u></a></p>
16:00 - 16:30	<p>Room: Renen <b>Panel Discussion</b></p> <p>Session Moderator: Per Henricsson, Elektroniktidningen (Swedish Magazine)</p> <p>Panel: Pieter J. Hazewindus, Synopsys, USA Tryggve Mathiesen, InformASIC, Sweden Mike Dini, Dinigroup, USA</p>	
16:30 - 18:00	<p>Snacks and mingle <b>Sponsored by Altera</b></p>	

## **Location**

ÅF

Frösundaleden 2A , 169 70 Solna, SWEDEN