

08:30	Registration
09:00 - 09:15	Conference opening Lennart Lindh, FPGAworld Timo D. Hämäläinen, Tampere University of Technology
09:15 - 10:00	Key Note Session <a href="#">Model-Based Design for FPGAs - Fact or Fiction?</a> <a href="#">Mike Woodward, MathWorks</a>
10:00	Coffee Break
10:30 - 12:00	Session Chair
	Session A1 <a href="#">Will ARM based FPGA SOC change system design Methodology?</a>
	Session A2 <a href="#">FPGA SoCs - A Centerpiece For Digital Design</a>
	Session A3 <a href="#">Design reuse of both HW and SW - will this finally take off?</a>
12:00	Lunch Break
13:00 - 14:00	Session Chair Vincent Mooney
	Key Note Session <a href="#">Requirement oriented hardware</a> <a href="#">Prof. Dr.-Ing. Michael Hübner</a>
14:00	Coffee Break
14:30 - 16:00	Session Chair Vincent Mooney
	Session B1 <a href="#">Reconfigure router design and evaluation for the FPGA-friendly SoCWire Network-On-Chip</a>
	Session B2 <a href="#">Interface Tasks Oriented 8-bit Soft-Core Processor</a>
	Session B3 <a href="#">MCAPI abstraction on FPGA based SoC design</a>
16:00	Conference close