

Call for FPGAworld Conference September 2013

International Academic/Industrial Papers, Product Presentations,
Exhibits, Demonstrations, Panel session and Tutorials
Stockholm, Copenhagen and Finland

Submissions should be in at least one of these areas

- DESIGN METHODS - MODELS AND PRACTICES
 - Project methodology
 - Design methods as Hardware/software co-design
 - Modeling of different abstraction
 - IP component designs
 - Interface design: supporting modularity
 - Integration - models and practices
 - Verification and validation
 - Board layout and verification
- TOOLS
 - News
 - Design, modeling, implementation, verification and validation
 - Instrumentation, monitoring, testing, debugging, etc.
 - Synthesis, compilers and languages
- HW/SW IP COMPONENTS
 - New IP components for platforms and applications
 - Real-time operating systems, file systems, internet communications
- PLATFORM ARCHITECTURES
 - Embedded systems
 - Single/multiprocessor architecture
 - Memory architectures
 - Reconfigurable architectures
 - HW/SW architecture
 - Low power architectures
- APPLICATIONS
 - Case studies from users in industry, academic and students
 - HW/SW component presentation
 - Prototyping
- SURVEYS, TRENDS AND EDUCATION
 - History and surveys
 - Tutorials
 - Student works and projects

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FPGATM
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Welcome to 9th FPGAworld CONFERENCE! September 6th 2012 Copenhagen, Technical University of Denmark

The international FPGAworld Conference addresses all aspects of digital and hardware/software system engineering on FPGA technology. It is a discussion and network forum for researchers and engineers working on industrial and research projects, state-of-the-art investigations, development and applications. For further information, please visit www.fpgaworld.com and www.facebook.com/fpgaworld.

EXHIBITORS



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08:30 - 09:00	Registration
09:00 - 09:15	Conference opening Lennart Lindh, FPGAworld Lars Dittmann, DTU (Technical University of Denmark)
09:15 - 10:00	<p>Key Note Session Heterogeneous computing in FPGAs Göran Bilski, Xilinx</p> <p>In an ever changing world with demands for faster development, more complex and high-performance applications, a heterogeneous computing approach is in many cases the best way to handle all these requirements. Performance, real-time response, footprint, cost, power, and development effort are among the most important requirements on designs today. Attempting to solve these with just one way of doing computations will make it hard to meet all requirements. This presentation will focus on how heterogeneous computing can be used in FPGAs to partition an embedded system in order to archive the application goals.</p>
10:00	Coffee Break
10:30 - 12:00	<p>Session Chair Lars Dittmann, DTU</p>
	<p>Session C1 Microsemi SmartFusion SOC</p> <p>Session A1 The Evolution of FPGA Design Complexity; a Software Perspective</p> <p>Session B1 Behavioral specification diversification for logic controllers implemented in FPGA devices</p>
12:00 - 13:00	Lunch Break

	<p>Session Chair David Bauman, Xelmo</p>
13:00 - 14:30	<p>Session A2 Will ARM based FPGA SOC change system design Methodology?</p> <p>Session A3 FPGA SoCs - A Centerpiece For Digital Design</p> <p>Session A4 Model-Based Design for FPGAs - Fact or Fiction?</p>
14:30 - 15:00	<p>Coffee Break</p>
15:00 - 16:00	<p>Session Chair Tryggve Mathiesen, InformASIC</p> <p>Session A5 Secure Remote Update of Embedded Hardware & Software</p> <p>Session A6 Zynq-7000 All Programmable SoC</p>
16:00 - 16:30	<p>Panel Discussion Session Moderator: Rolf Sylvester-Hvid Aktuel Elektronik (Magazine), Denmark</p> <p>Panel: Göran Bilski, XILINX, Sweden Tryggve Mathiesen, InformASIC, Sweden Mike Dini, Dinigroup, USA</p>

Location

DTU (SCION)
Building 372
Diplomvej
2800 Lyngby

Date: September 6th 2012

